

ABSTRACT OF THE DISCLOSURE

A register file includes a plurality of registers for storing therein data, a plurality of input ports for receiving therethrough the data to be stored in the registers, and a plurality of output ports for delivering therethrough the data stored in the registers. Each register includes an input port selector for selecting one of the write ports through which data is received. The register file also includes a read data selector block for specifying which data stored in the registers is to be read through one of the output ports. The output port selector is implemented by a combinational circuit which saves power dissipation of the register file.

